IN THE CLAIMS:

Please amend the claims as follows:

1.   (Currently Amended) A codec circuit having a programmable digital bandpass filter, for matching the filter characteristics of the codec circuit to a transmitted PCM signal, having at least one programmable digital high-pass filter and at least one programmable digital low-pass filter connected in series, wherein setting filter coefficients for the programmable digital high-pass and low-pass filters [[can]] are each [[be]] set based on identification of a PCM signal transmitted through the codec circuit, wherein the PCM signal is identified by means of a signal identification device for identification of a configured to identify the PCM signal transmitted through the codec circuit, as a function of the transmitted PCM signal in order to vary a bandpass filter characteristic for the programmable digital bandpass filter.

2.   (Previously Presented) The codec circuit as claimed in claim 1, wherein the setting filter coefficients are stored in coefficient memory devices which are associated with the programmable digital high-pass and low-pass filters.

3.   (Previously Presented) The codec circuit as claimed in claim 2, wherein the memory devices are random access memories (RAM).

4.   (Previously Presented) The codec circuit as claimed in claim 2, wherein the memory devices are connected via coefficient setting lines to the signal identification device.

5.   (Previously Presented) The codec circuit as claimed in claim 1, wherein the programmable digital filters are each seventh-order filters.

6.   (Previously Presented) The codec circuit as claimed in claim 1, wherein the upper and lower signal transmission cut-off frequencies of the bandpass filter and the gradient of bandpass filter flanks are set by means of the setting filter coefficients.

7.   (Currently Amended) The codec circuit as claimed in claim 6, wherein the lower signal transmission cut-off frequency ~~can be~~ is set by setting the setting filter coefficients of the digital high-pass filter.

8.   (Currently Amended) The codec circuit as claimed in claim 6, wherein the upper signal transmission cut-off frequency ~~can be~~ is set by setting the setting filter coefficients of the programmable digital low-pass filter.

9.   (Previously Presented) The codec circuit as claimed in claim 1, wherein a frequency response correction filter is also provided, in order to compensate for the ripple in the bandpass filter characteristic in the passband.